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<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	09/993,204	POPE, JOHN	
	Examiner	Art Unit	
	Sudhanshu C. Pathak	2611	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Jan. 3<sup>rd</sup>, 2007.
2. ☒ The allowed claim(s) is/are 6, 9 & 18-23 and re-numbered as claims 1-8 respectively.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

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|---|--|
| <ol style="list-style-type: none"> <li>1. <input type="checkbox"/> Notice of References Cited (PTO-892)</li> <li>2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),<br/>Paper No./Mail Date _____</li> <li>4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br/>of Biological Material</li> </ol> | <ol style="list-style-type: none"> <li>5. <input type="checkbox"/> Notice of Informal Patent Application</li> <li>6. <input type="checkbox"/> Interview Summary (PTO-413),<br/>Paper No./Mail Date _____</li> <li>7. <input type="checkbox"/> Examiner's Amendment/Comment</li> <li>8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance</li> <li>9. <input type="checkbox"/> Other _____</li> </ol> |
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**DETAILED ACTION**

1. Claims 6, 9 & 18-23 are pending in the application.
2. Claims 1-5, 7-8 & 10-17 have been canceled.

***Allowable Subject Matter***

3. Claims 6, 9 & 18-23 are allowed.
4. Claims 6, 9 & 18-23 and re-numbered as claims 1-8 respectively are allowable over the prior art of record because the cited references do not contain the specified limitation of a rake receiver circuit for receiving multi-path signals, the rake receiver comprising: a first rake finger circuit having a first variable delay element, where the first variable delay element is configured to receive a first delay control signal, the first delay control signal value being selected to align a first delay introduced by the first variable delay element with a first multi-path signal to produce a first correlated data signal; a second rake finger circuit having a second variable delay element, where the second variable delay element is configured to receive a second delay control signal, the second delay control signal value being selected to align a second delay introduced by the second variable delay element with a second multi-path signal to produce a second correlated data signal; a scan control circuit configured to receive the first and second correlated signals and, responsive thereto, generate the first and second delay control signals, where the scan control circuit is configured to generate the first delay control signal by: (i) varying the first delay control signal over a first predetermined range of values, (ii) measuring a signal power level of the first

correlated data signal to determine a value of the first delay control signal corresponding to a highest measured signal power level of the first correlated data signal, and (iii) setting the first delay control signal to the value of the first delay control signal corresponding to the highest measured signal power level of the first correlated data signal for operation, and where the scan control circuit is further configured to generate the second delay control signal by: (i) varying the second delay control signal over a second predetermined range of values, (ii) measuring a signal power level of the second correlated data signal to determine a value of the second delay control signal corresponding to a highest measured signal power level of the second correlated data signal, and (iii) setting the second delay control signal to the value of the second delay control signal corresponding to the highest measured signal power level of the second correlated data signal for operation; and a summing circuit for summing the first and second correlated signals to produce a combined data signal.

### ***Conclusion***

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sudhanshu C. Pathak whose telephone number is (571)-272-3038.

The examiner can normally be reached on M-F: 9am-6pm. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chieh M. Fan can be reached on (571)-272-3042.

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The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Sudhanshu C. Pathak  
Examiner  
Art Unit 2611

  
CHIEH M. FAN  
SUPERVISORY PATENT EXAMINER